

Bluetooth LE Module MS50SFA





Datasheet v 1.0.0

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Version Note

Version	Details	Contributor(s)	Date	Notes
1.0.0	First edit	Michelle, Leo	2024.05.10	

Part Numbers

Model	Hardware Code	
MS50SFA1	1Y32AI	
MS50SFA2	2Y32AI	





MS50SFA-nRF52832

Small size, fully certified, cost-effective Bluetooth 5.4 module

The MS50SFA is an RF transceiver using the nRF52832 which has an ARM core Cortex-M4F, the MCU has a faster operating speed, with the core running at up to 64Mhz. 512KB of FLASH program space, 64KB of RAM and other powerful supporting resources. It is suitable for low-power systems, ultra-low sleep current and low power consumption at runtime.

FEATURES







Small-size



Fully certified



High quality -price ratio

KEY PARAMETER

MS50SFA-nRF52810			
Chip Model	nRF52832	Antenna	PCB/Ceramic
Module Size	15.8×12×2mm	GPIO	13
Flash	512KB	RAM	64KB
Receiving Sensitivity	-96dBm	Transmission Power	-40 ~ +4dBm
Current(TX)	0dBm- 5.3mA	Current(RX)	5.4mA

APPLICATION



Smart Buildings



Consumer Electronics



Smart Healthcare



Smart Agriculture



Security Equipment



Automotive Equipment

CERTIFICATION

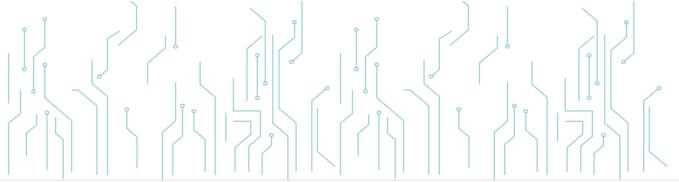


TELEC SRRC



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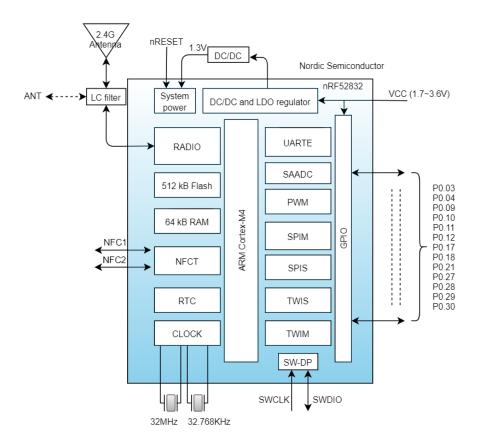
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1 BLOCK DIAGRAM

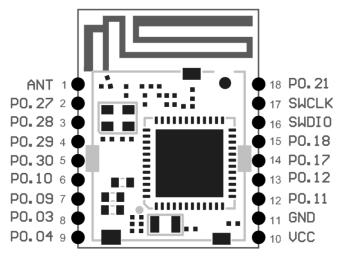


2 ELECTRICAL SPECIFICATION

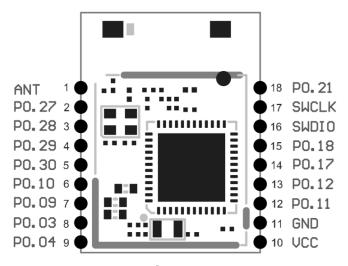
Parameters	Value	Notes
Working Voltage	1.7V-3.6V	To ensure RF work, supply voltage suggest not lower than 2.3V
Working Temperature	-40°C~+85°C	Storage temperature is -40 °C ~+125 °C
Transmission Power	-40 ~ +4dBm	Configurable
Current(RX)	5.4mA	RF receiving current under 1Mbps pattern
Current(TX)	5.3mA	RF transmission current under odB pattern
Module Dimension	15.8*12*2mm	
Quantity of IO Port	13	



3 PIN DESCRIPTION



PCB Antenna



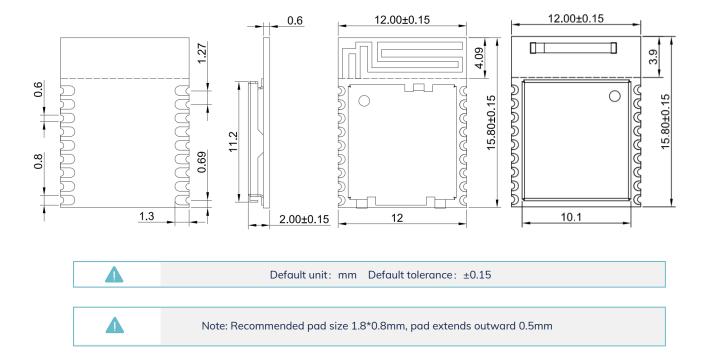
Ceramic Antenna

4 PIN DEFINITION

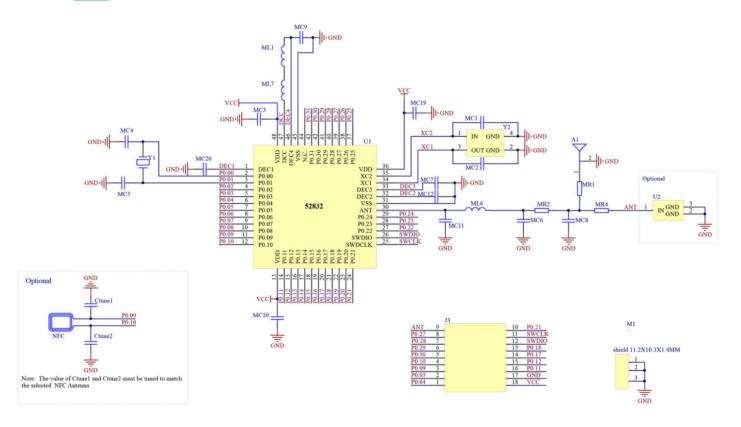
Pin Number	Symbol	Туре	Definition
1	ANT	External antenna pins	Using the module's built-in antenna, this pin is directly suspended. If not using the built-in antenna of the module, an external antenna can be connected through this pin. When using an external antenna, the resistor that is connected to the antenna needs to be horizontally soldered to this pin.
2~9	P0.27 to P0.04	GPIO	General purpose I/O ports, where P0.28,P0.29,P0.30, P0.03,P0.04 are analog pins that can be used as ADC
10	VCC	Power source	
11	GND	Ground	Ground
12-15	P0.11 to P0.18	GPIO	General purpose I/O ports
16/17	SWCLK/SWDIO	Debug	Debug, when debug only need to connect power supply pin, ground and these 2 pins.
18	P0.21	GPIO/RESET	This pin can be reused as a hardware reset pin



5 MECHANICAL DRAWING



6 ELECTRICAL SCHEMATIC



Notice: Before placing an order, please confirm the specific configuration required with the salesperson.



7 PCB LAYOUT

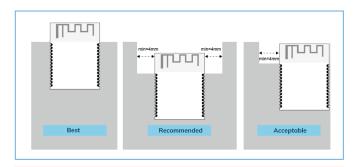
There should be no GND plane or metal cross wiring in the module antenna area, and components should not be placed nearby. It is best to make a hollow or clear area, or place it on the edge of the PCB board.

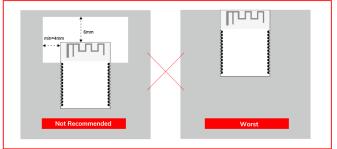


Notice: Refer to examples as below, and highly suggest to use the first design and the adjustment of modules antenna design according to the first wiring.

Layout Notes:

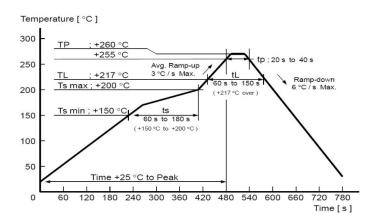
- 1) Preferred Module antenna area completely clearance and not be prevented by metals, otherwise it will influence antenna's effect (as above DWG. indication).
- 2) Cover the external part of module antenna area with copper as far as possible to reduce the main board's signal cable and other disturbing.
- 3) It is preferred to have a clearance area of 4 square meter or more area around the module antenna (including the shell) to reduce the influence to antenna.
- 4) Device should be grounded well to reduce the parasitic inductance.
- 5) Do not cover copper under module's antenna in order to avoid affect signal radiation or lead to transmission distance affected.
- 6) Antenna should keep far from other circuits to prevent radiation efficiency reduction or affects the normal operation of other lines.
- 7) Module should be placed on edge of circuit board and keep a distance away from other circuits.
- 8) Suggesting to use magnetic beads to insulate module's access power supply.

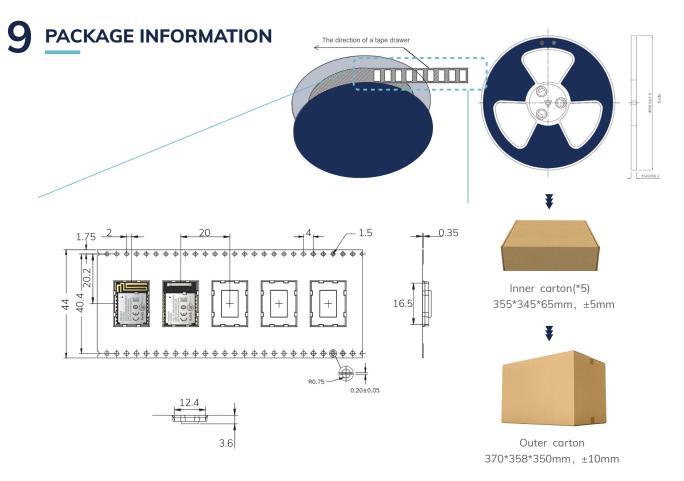




8 REFLOW AND SOLDERING

- 1) Do SMT according to above reflow oven temperature deal curve. Max. Temperature is $260\,^{\circ}\mathrm{C}$; Refer to IPC/JEDEC standard; Peak TEMP< $260\,^{\circ}\mathrm{C}$; Times: ≤ 2 times, suggest only do once reflow soldering on module surface in case of SMT double pad involved. Contact us if special crafts involved.
- 2) Suggesting to make 0.2mm thickness of module SMT for partial ladder steel mesh, then make the opening extend 0.8mm
- 3) After unsealing, it cannot be used up at one time, should be vacuumed for storage, couldn't be exposed in the air for long time. Please avoid getting damp and soldering-pan oxidizing. If there are 7 to 30 days interval before using online SMT, suggest to bake at 65-70 °C for 24 hours without disassembling the tape.
- 4) Before using SMT, please adopt ESD protection measure.





Remarks

General material list for FCL packaging:



Carrier tape packaging tray



Inner carton(*5) 355*345*65mm, ±5mm



Humidity Indicator (1 pcs/bag)

Outer carton 370*358*350mm, ±10mm



Desiccant (placed in a vacuum bag)



Vacuum bag

Other:

Moisture-proof label (attached to the vacuum bag) Certification label (attached to the vacuum bag) Outer box label

Default unit: mm Default tolerance: ±0.1

Packing detail	Specification	Net weight	Gross weight	Dimension
MS50SFA	850PCS	425g	1300g	W=44mm, T=0.35mm



Note: Default weight tolerance all are within 10g $\,(\,$ except the special notes)



10 STORAGE CONDITIONS

- Please use this product within 6 months after signing the receipt.
 - This product should be stored without opening the package at an ambient temperature of $5\sim35^{\circ}$ C and a humidity of $20\sim70\%$ RH.
 - This product should be left for more than 6 months after receipt and should be confirmed before use.
 - The product must be stored in a non-corrosive gas (CI2, NH3, SO2, NOx, etc.).
 - To avoid damaging the packaging material, do not apply any excessive mechanical shocks, including but not limited to sharp objects adhering to the packaging material and product dropping.
- This product is suitable for MSL2 (based on JEDEC standard J-STD-020).
 - After opening the package, the product must be stored at \leq 30°C/<60%RH. It is recommended to use the product within 3-6 months after opening the package.
 - When the color of the indicator in the package changes, the product should be baked before welding.
- Baking is not required for one year if exposure is limited to <30°C and 60%RH. Refer to MSL2 for exposure criteria for moisture sensitivity level. If exposed to (≥168h@85°C/60%RH) conditions or stored for more than one year, recommended baking conditions.
 - 1. 120 +5/-5°C, 8 hours, 1 time

Products must be baked individually on heat-resistant trays because the materials (base tape, reel tape, and cover tape) are not heat-resistant, and the packaging material may be deformed at temperatures of $120\,\text{C}$; $2\times90\,\text{C}$ +8/-0 $\,\text{C}$, 24hours, 1times

The base tape can be baked together with the product at this temperature. Please pay attention to the uniformity of heat.

11 HANDLING CONDITIONS

- Be careful in handling or transporting products because excessive stress or mechanical shock may break products.
- Handle with care if products may have cracks or damages on their terminals. If there is any such damage, the characteristics of products may change. Do not touch products with bare hands that may result in poor solder ability and destroy by static electrical charge.

12 QUALITY

Cognizant of our commitment to quality, we operate our own factory equipped with state-of-the-art production facilities and a meticulous quality management system. We hold certifications for ISO9001, ISO14001, ISO27001, OHSA18001, BSCI.

Every product undergoes stringent testing, including transmit power, sensitivity, power consumption, stability, and aging tests. Our fully automated module production line is now in full operation, boasting a production capacity in the millions, capable of meeting high-volume production demands.

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13 COPYRIGHT STATEMENT

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14 RELATED DOCUMENTS

- nRF52832_Chip_Datasheet
 https://en.minewsemi.com/file/nRF52832_Chip_Datasheet_EN.pdf
- MinewSemi_Product_Naming_Reference_Manual_V1.0
 https://en.minewsemi.com/file/MinewSemi_Product_Naming_Reference_Manual_EN.pdf
- MinewSemi_Connectivity_Module_Catalogue_V2.0
 https://en.minewsemi.com/file/MinewSemi_Connectivity_Module_Catalogue_EN.pdf



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