

Bluetooth LE Module **MS45SF1**



Datasheet
V 1.0.0



Version Note

Version	Details	Contributor(s)	Date	Notes
1.0.0	First edit	Michelle, Leo	2024.05.10	

Part Number

Model	Hardware Code
MS45SF11	1Y40AI

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https://en.minewsemi.com/file/MS45SF1-nRF5340_Datasheet_K_EN.pdf



MS45SF1-nRF5340

Dual Core, Ultra Low Power, Full IO Port, High Performance Bluetooth 5.4 Module

The MS45SF1 is a chip Bluetooth 5.4 module designed using the Nordic nRF5340. It has 1 MB FLASH, 512 KB RAM, a floating point unit (FPU), an 8 KB 2-way associative cache, and DSP instruction functions; the network processor has a frequency of 64 MHz and is optimized for low power consumption and efficiency, and it comes with 256 KB FLASH and 64 KB RAM, enabling more powerful computing capabilities and floating point technologies. It can realize very complex algorithms. It is suitable for Bluetooth low-power applications and Bluetooth high-end applications.

FEATURES



Bluetooth 5.4



Dual core



All IO ports



Ultra-low power consumption



High Performance

KEY PARAMETES

MS45SF1-nRF5340			
Chip Model	nRF5340	Antenna	PCB
Module Size	18×12.5×2mm	GPIO	48
Flash	1M+256KB	RAM	512KB+64KB
Receiving Sensitivity	-98dBm (1Mbps) ; -104dBm (125kbps)	Transmission Power	-40 ~ +3dBm
Current(TX)	0dBm- 3.2mA	Current(RX)	2.6mA

APPLICATION



Smart Buildings



Consumer Electronics



Smart Healthcare



Smart Agriculture



Security Equipment



Automotive Equipment

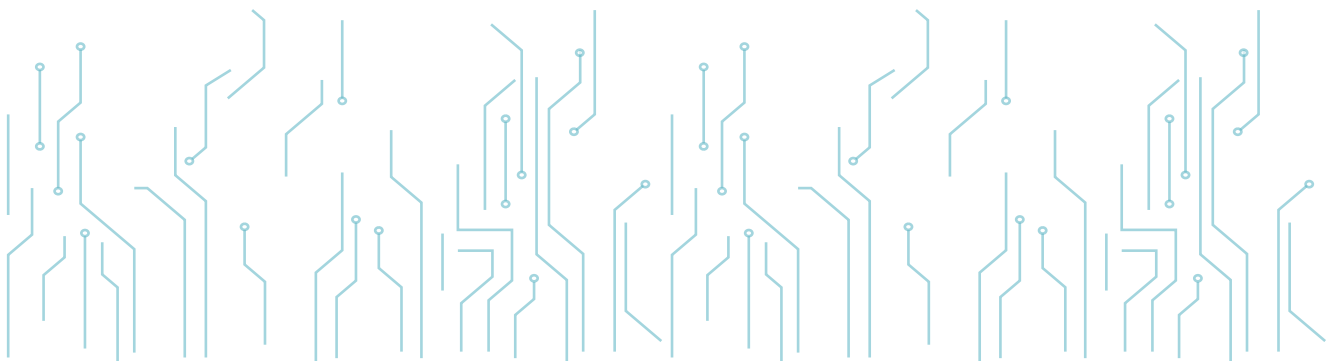
CERTIFICATION



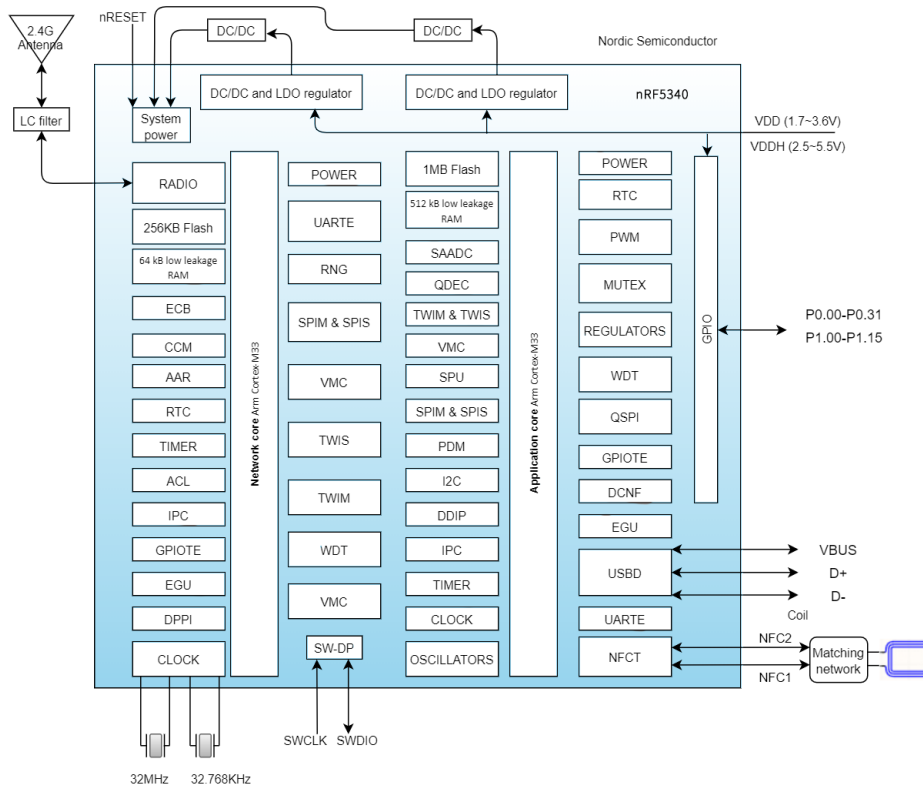


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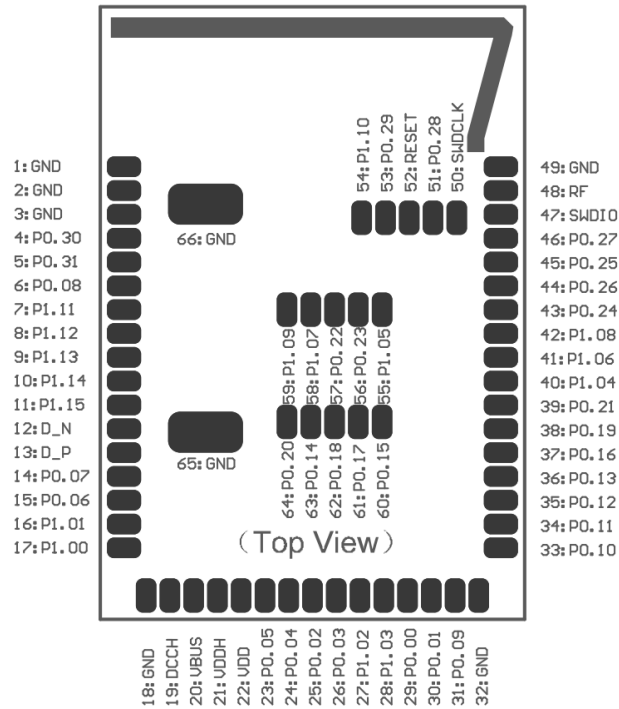
1 BLOCK DIAGRAM



2 ELECTRICAL SPECIFICATION

Parameters	Value	Notes
Working Voltage	1.7V-5.5V	To ensure RF work, supply voltage suggest not lower than 3V
Working Temperature	-40 C ~+85 C	Storage temperature is -40 C ~+125 C
Transmission Power	-40 ~ +3dBm	Configurable
Current(RX)	2.6mA	RF receiving current under 1Mbps pattern
Current(TX)	3.2mA	RF transmission current under odB pattern
Module Dimension	18*12.5*2mm	
Quantity of IO Port	48	

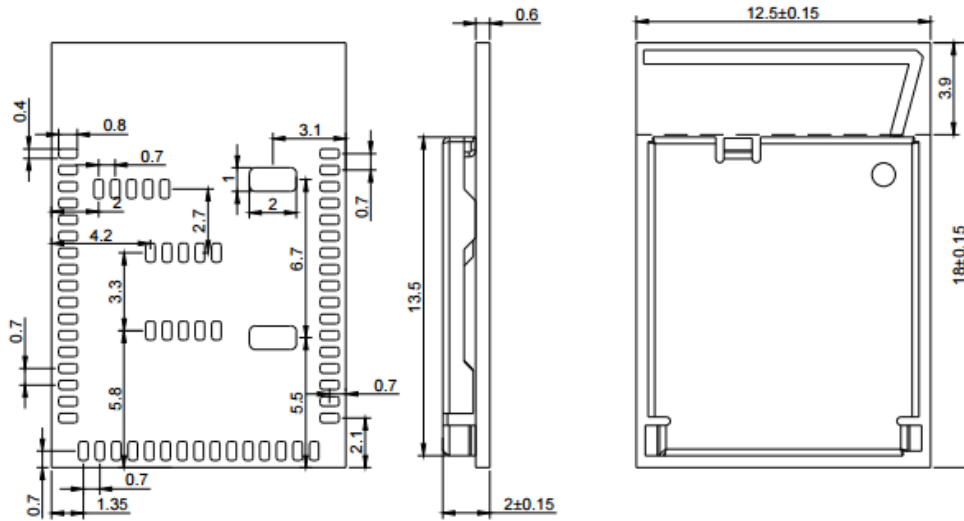
3 PIN DESCRIPTION



4 PIN DEFINITION

Pin Number	Symbol	Type	Definition
22	VDD	Power	Power supply 1.7-3.6V
21	VDDH	Power	Power supply 2.5-5.5V
20	VBUS	Power	Power conversion access required for USB port
1/2/3/18/32 /49/65/66	GND	Power	Ground
50	SWDCLK	Debug	Serial wire debug clock input for debug and programming
47	SWDIO	Debug	Serial wire debug I/O for debug and programming
19	DCCH	Power	DC/DC Conversion Output
4-11/14-17/23-31 /33-46/51/53-64	GPIO	Digital I/O	General purpose I/O
13	D+	USB	USB D+
12	D-	USB	USB D-

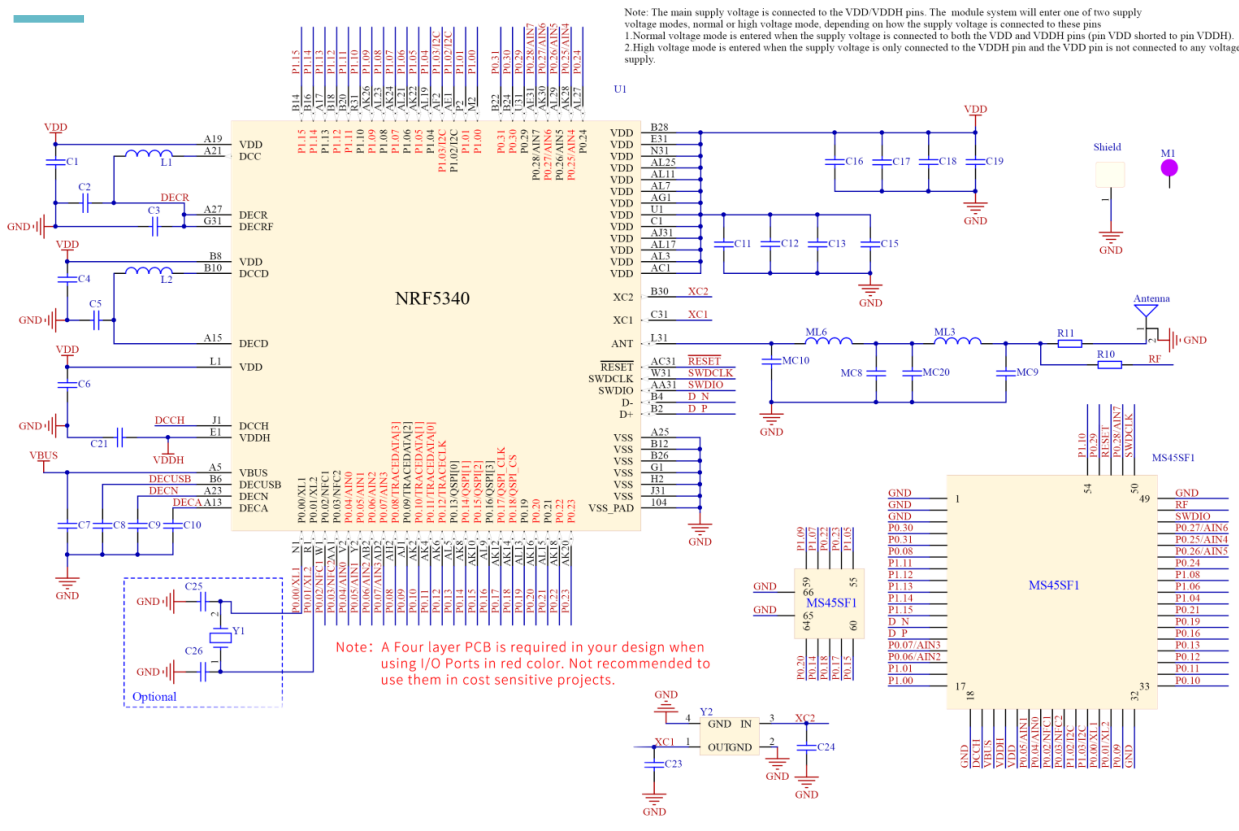
5 MECHANICAL DRAWING



⚠ Default unit: mm Default tolerance: ±0.15

⚠ Note: Recommended pad size 1.8*0.8mm, pad extends outward 0.5mm


6 ELECTRICAL SCHEMATIC



⚠ Notice: Before placing an order, please confirm the specific configuration required with the salesperson.

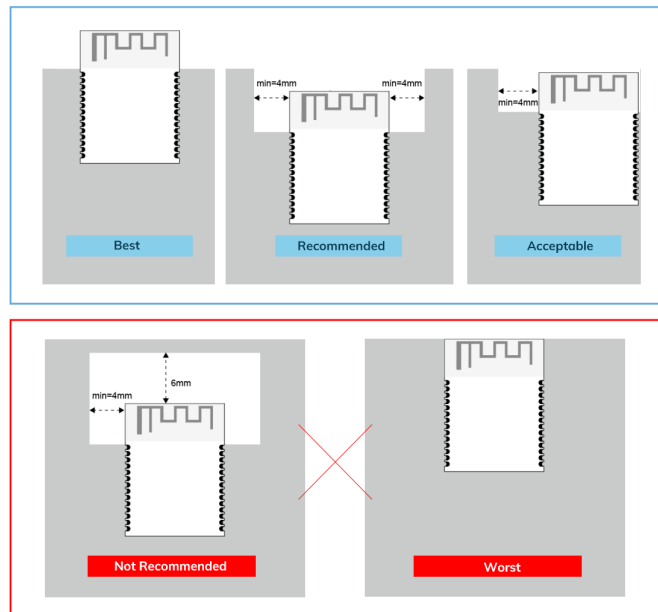
7 PCB LAYOUT

There should be no GND plane or metal cross wiring in the module antenna area, and components should not be placed nearby. It is best to make a hollow or clear area, or place it on the edge of the PCB board.

 Notice: Refer to examples as below, and highly suggest to use the first design and the adjustment of modules antenna design according to the first wiring.

Layout Notes:

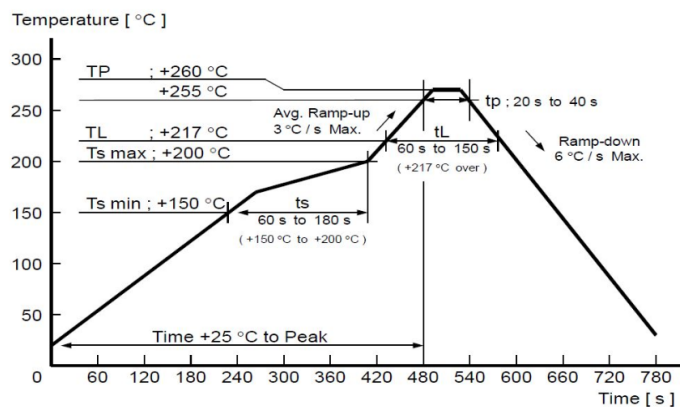
- 1) Preferred Module antenna area completely clearance and not be prevented by metals, otherwise it will influence antenna's effect (as above DWG. indication).
- 2) Cover the external part of module antenna area with copper as far as possible to reduce the main board's signal cable and other disturbing.
- 3) It is preferred to have a clearance area of 4 square meter or more area around the module antenna (including the shell) to reduce the influence to antenna.
- 4) Device should be grounded well to reduce the parasitic inductance.
- 5) Do not cover copper under module's antenna in order to avoid affect signal radiation or lead to transmission distance affected.
- 6) Antenna should keep far from other circuits to prevent radiation efficiency reduction or affects the normal operation of other lines.
- 7) Module should be placed on edge of circuit board and keep a distance away from other circuits.
- 8) Suggesting to use magnetic beads to insulate module's access power supply.



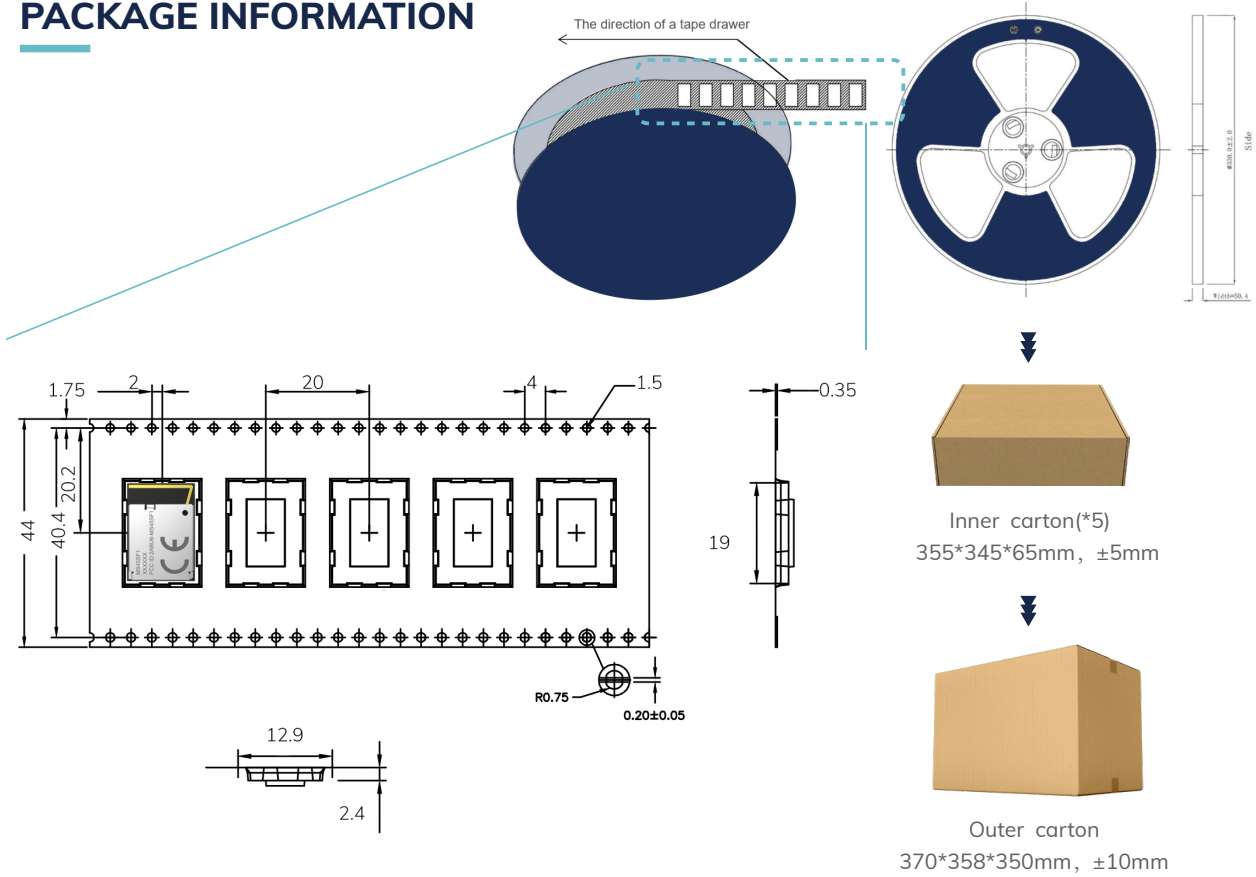
8 REFLOW AND SOLDERING

1) Do SMT according to above reflow oven temperature deal curve. Max. Temperature is 260 °C; Refer to IPC/JEDEC standard; Peak TEMP<260 °C ; Times: ≤2 times, suggest only do once reflow soldering on module surface in case of SMT double pad involved. Contact us if special crafts involved.

- 2) Suggesting to make 0.2mm thickness of module SMT for partial ladder steel mesh, then make the opening extend 0.8mm
- 3) After unsealing, it cannot be used up at one time, should be vacuumed for storage, couldn't be exposed in the air for long time. Please avoid getting damp and soldering-pan oxidizing. If there are 7 to 30 days interval before using online SMT, suggest to bake at 65-70 °C for 24 hours without disassembling the tape.
- 4) Before using SMT, please adopt ESD protection measure.

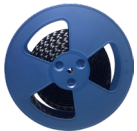


9 PACKAGE INFORMATION

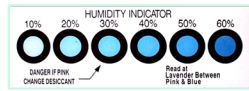


Remarks

General material list for FCL packaging:



Carrier tape packaging tray



Humidity Indicator
(1 pcs/bag)



Desiccant
(placed in a vacuum bag)



Vacuum bag



Inner carton(*5)
355*345*65mm, ±5mm



Outer carton
370*358*350mm, ±10mm

Other:

Moisture-proof label (attached to the vacuum bag)

Certification label (attached to the vacuum bag)

Outer box label

Default unit: mm Default tolerance: ±0.1

Packing detail	Specification	Net weight	Gross weight	Dimension
MS45SF1	850PCS	723g	1183g	W=44mm, T=0.35mm

Note: Default weight tolerance all are within 10g (except the special notes)



10 STORAGE CONDITIONS

- Please use this product within 6 months after signing the receipt.
 - This product should be stored without opening the package at an ambient temperature of 5~35°C and a humidity of 20~70%RH.
 - This product should be left for more than 6 months after receipt and should be confirmed before use.
 - The product must be stored in a non-corrosive gas (Cl₂, NH₃, SO₂, NO_x, etc.).
 - To avoid damaging the packaging material, do not apply any excessive mechanical shocks, including but not limited to sharp objects adhering to the packaging material and product dropping.
- This product is suitable for MSL2 (based on JEDEC standard J-STD-020).
 - After opening the package, the product must be stored at ≤30°C/<60%RH. It is recommended to use the product within 3-6 months after opening the package.
 - When the color of the indicator in the package changes, the product should be baked before welding.
- Baking is not required for one year if exposure is limited to <30°C and 60%RH. Refer to MSL2 for exposure criteria for moisture sensitivity level. If exposed to (≥168h@85°C/60%RH) conditions or stored for more than one year, recommended baking conditions.
 1. 120 +5/-5°C, 8 hours, 1 timeProducts must be baked individually on heat-resistant trays because the materials (base tape, reel tape, and cover tape) are not heat-resistant, and the packaging material may be deformed at temperatures of 120°C;
 2. 90°C +8/-0°C, 24hours, 1timesThe base tape can be baked together with the product at this temperature. Please pay attention to the uniformity of heat.

11 HANDLING CONDITIONS

- Be careful in handling or transporting products because excessive stress or mechanical shock may break products.
- Handle with care if products may have cracks or damages on their terminals. If there is any such damage, the characteristics of products may change. Do not touch products with bare hands that may result in poor solder ability and destroy by static electrical charge.

12 QUALITY

Cognizant of our commitment to quality, we operate our own factory equipped with state-of-the-art production facilities and a meticulous quality management system. We hold certifications for ISO9001, ISO14001, ISO27001, OHSAS18001, BSCI.

Every product undergoes stringent testing, including transmit power, sensitivity, power consumption, stability, and aging tests. Our fully automated module production line is now in full operation, boasting a production capacity in the millions, capable of meeting high-volume production demands.

13 COPYRIGHT STATEMENT

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14 RELATED DOCUMENTS

- nRF5340_Chip_Datasheet
https://en.minewsemi.com/file/nRF5340_Chip_Datasheet_EN.pdf
- MinewSemi_Product_Naming_Reference_Manual_V1.0
https://en.minewsemi.com/file/MinewSemi_Product_Naming_Reference_Manual_EN.pdf
- MinewSemi_Connectivity_Module_Catalogue_V2.0
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